

## Course: Digital Electronics 2

credits: 2

<b>Course code</b>	ELVP19ADIG2	<b>Modes of delivery</b>	Lecture
<b>Name</b>	Digital Electronics 2		Practical / Training
<b>Study year</b>	2020-2021	<b>Assessments</b>	Digital Electronics 2 - Written, organised by STAD examinations
<b>ECTS credits</b>	2		Digital Electronics 2 Labs - Skills test
<b>Language</b>	English		
<b>Coordinator</b>	P.J. Kamphuis		

### Learning outcomes

The student is able to:

- Design and build shift registers and counters using standard memory elements.
- Design and build state machines with hardware.
- Explain the principle of a state machine.

### Content

During this unit the student will learn about Boolean algebra, combinatory logic and state machines. This will enable the student to understand the architecture and internal operation of a CPU and its peripherals.

During the theory lessons a few practical's are performed. (Practical is not graded).

### Included in programme(s)

Electrical Engineering Major Sensor Technology

### School(s)

Institute of Engineering

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