

Course: Digital Electronics 2

credits: 2

Course code	ELVP19ADIG2	Modes of delivery	Lecture Practical / Training
Name	Digital Electronics 2	Assessments	Digital Electronics 2 - Written, organised by STAD examinations Digital Electronics 2 Labs - Skills test
Study year	2020-2021		
ECTS credits	2		
Language	English		
Coordinator	P.J. Kamphuis		

Learning outcomes

The student is able to:

- Design and build shift registers and counters using standard memory elements.
- Design and build state machines with hardware.
- Explain the principle of a state machine.

Content

During this unit the student will learn about Boolean algebra, combinatory logic and state machines. This will enable the student to understand the architecture and internal operation of a CPU and its peripherals.

During the theory lessons a few practical's are performed. (Practical is not graded).

Included in programme(s)

Electrical Engineering Major Sensor Technology

School(s)

Institute of Engineering

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